

REGISTRAR



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No.ANU/Acad./U.G/CBCS/III Electronics/SEM-VI/Syllabus/2017

Date: 30-10-2017

PROCEEDINGS OF THE VICE-CHANCELLOR

Sub:- ANU – Academic –UG courses –CBCS - III B.Sc Electronics VI semester syllabus - Approval - Orders – Issued.

Ref:- 1. Minutes of the meeting of the Board of Studies (UG) in Electronics held on 25-07-2017.

2. Vice-Chancellor's orders dated 29-10-2017.

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ORDER:-

The Vice-Chancellor, after having considered the minutes 1st cited, has approved the III B.Sc. Electronics VI semester syllabus and model question paper for the academic year 2017-18 prepared by the Board of Studies (UG) in Electronics.

Semester – VI

Elective Paper:

1. Paper VII: Micro Controller and Interfacing

Any one of the Cluster Elective Papers A or B:

2. Paper VIII (A1): Embedded Systems Design

3. Paper IX (A2): Electronic Instrumentation

4. Paper X (A3): Power Electronics

OR

Paper (B): PC Maintenance and Trouble Shooting

5. Paper VIII(B1): Computer Networks

6. Paper IX(B2): Consumer Electronics

7. Paper X(B3): Optical Fiber Communication and It's Application

Labs:

Lab -7: Microcontroller Lab

Lab-8: Electronic Instrumentation Lab

Lab-9: Power Electronics Lab

Lab-10: Project Work

(BY ORDER)

**JOINT REGISTRAR
Academic**

To

The Chairman and all members, Board of Studies (UG) in Electronics, ANU.

All the Principals of the Affiliated Colleges under ANU area.

Copy to:

The Dean, Faculty of Physical Science, ANU.

The Dean, CDC, ANU.

The Coordinator, UG (Exams), ANU

The Addl. Controller of Examinations, ANU.

The P.A. to Vice-Chancellor/ Registrar/Rector, ANU.